

<b>Notice of References Cited</b>	Application/Control No. 09/692,606	Applicant(s)/Patent Under Reexamination CHU ET AL.
	Examiner Erik Kielin	Art Unit 2813

**U.S. PATENT DOCUMENTS**

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
A	US-			
B	US-			
C	US-			
D	US-			
E	US-			
F	US-			
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**FOREIGN PATENT DOCUMENTS**

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N					
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S					
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**NON-PATENT DOCUMENTS**

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
U	Wolf, et al. Silicon Processing for the VLSI Era, Vol. 1-Process Technology, 2nd ed., Lattice Press: Sunset Beach CA, 2000, pp. 386-391.
V	
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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